

STGP14NC60KD - STGF14NC60KD STGB14NC60KD

N-CHANNEL 14A - 600V - TO-220/TO-220FP/D²PAK SHORT CIRCUIT RATED PowerMESH™ IGBT

Table 1: General Features

TYPE	V _{CES}	V _{CE(sat)} (Max) @25°C	I _C (#) @100°C
STGB14NC60KD	600 V	< 2.5 V	14 A
STGF14NC60KD	600 V	< 2.5 V	7 A
STGP14NC60KD	600 V	< 2.5 V	14 A

- LOWER ON-VOLTAGE DROP (Vcesat)
- OFF LOSSES INCLUDE TAIL CURRENT
- LOWER C_{RES} / C_{IES} RATIO
- SWITCHING LOSSES INCLUDE DIODE RECOVERY ENERGY
- VERY SOFT ULTRA FAST RECOVERY ANTIPARALLEL DIODE
- NEW GENERATION PRODUCTS WITH TIGHTER PARAMETER DISTRUBUTION

DESCRIPTION

Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESH™ IGBTs, with outstanding performances. The suffix "K" identifies a family optimized for high frequency motor control applications with short circuit withstand capability.

APPLICATIONS

- HIGH FREQUENCY INVERTERS
- SMPS and PFC IN BOTH HARD SWITCH AND RESONANT TOPOLOGIES
- MOTOR DRIVERS

Figure 1: Package

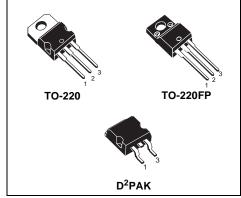


Figure 2: Internal Schematic Diagram

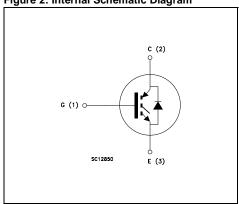


Table 2: Order Codes

SALES TYPE	MARKING	PACKAGE	PACKAGING
STGB14NC60KDT4	GB14NC60KD	D ² PAK	TAPE & REEL
STGF14NC60KD	GF14NC60KD	TO-220FP	TUBE
STGP14NC60KD	GP14NC60KD	TO-220	TUBE

Rev.2

July 2005 1/14

Table 3: Absolute Maximum ratings

Symbol	Parameter	Valu	ie	Unit
		STGB14NC60KD STGP14NC60KD	STGF14NC60KD	
VCES	Collector-Emitter Voltage (V _{GS} = 0)	60	0	V
VECR	Emitter-Collector Voltage	20)	V
V_{GE}	Gate-Emitter Voltage	±20		V
Ic	Collector Current (continuous) at T _C = 25°C (#)	25 11		Α
Ic	Collector Current (continuous) at T _C = 100°C (#)	14	7	Α
I _{CM} (•)	Collector Current (pulsed)	50)	Α
IF	Diode RMS Forward Current at T _C = 25°C	20)	Α
P _{TOT}	Total Dissipation at T _C = 25°C	80	25	W
	Derating Factor	0.64	0.20	W/°C
V _{ISO}	Insulation Withstand Voltage A.C.(t = 1 sec; Tc = 25°C)	2500		V
T _{stg}	Storage Temperature	– 55 to 150		°C
Tj	Operating Junction Temperature	- 55 10	, 130	C

^() Pulse width limited by Max Junction Temperature.

Table 4: Thermal Data

			Min.	Тур.	Max.	
Rthj-case	Thermal Resistance Junction-case	TO-220 D²PAK			1.56	°C/W
		TO-220FP			5.0	°C/W
Rthj-amb	Thermal Resistance Junction-ambient				62.5	°C/W
TL	Maximum Lead Temperature for Soldering Purpose (1.6 mm from case, for 10 sec.)			300		°C

ELECTRICAL CHARACTERISTICS (T_{CASE} =25°C UNLESS OTHERWISE SPECIFIED)

Table 5: Main Parameters

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{BR(CES)}	Collector-Emitter Breakdown Voltage	I _C = 1 mA, V _{GE} = 0	600			V
I _{CES}	Collector cut-off Current (V _{GE} = 0)	V _{CE} = Max Rating, T _C = 25°C V _{CE} = Max Rating, T _C = 125°C			10 1	μA mA
I _{GES}	Gate-Emitter Leakage Current (V _{CE} = 0)	V _{GE} = ±20V , V _{CE} = 0			±100	nA
V _{GE(th)}	Gate Threshold Voltage	$V_{CE}=V_{GE}, I_{C}=250 \mu A$	5		7	V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	V _{GE} = 15V, I _C = 7A V _{GE} = 15V, I _C = 7A, T _C = 125°C		2.0 1.8	2.5	V V

^(#) Calculated according to the iterative formula:

$$I_{C}(T_{C}) = \frac{T_{JMAX} - T_{C}}{R_{THJ-C} \times V_{CESAT(MAX)}(T_{C}, I_{C})}$$

ELECTRICAL CHARACTERISTICS (CONTINUED)

Table 6: Dynamic

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
g _{fs} (1)	Forward Transconductance	V _{CE} = 15 V _, I _C = 7 A		3		S
Cies	Input Capacitance	V _{CE} = 25 V, f= 1 MHz, V _{GE} = 0		760		pF
Coes	Output Capacitance			86		pF
C _{res}	Reverse Transfer Capacitance			15.5		pF
Q _g Q _{ge} Q _{gc}	Total Gate Charge Gate-Emitter Charge Gate-Collector Charge	$V_{CE} = 390 \text{ V}, I_{C} = 7 \text{ A}, V_{GE} = 15 \text{ V} $ (see Figure 21)		34.4 8.1 16.4		nC nC nC
t _{scw}	Short Circuit Withstand Time	$V_{CE} = 0.5 V_{BR(CES)}, T_j = 125^{\circ}C,$ $R_G = 10 \Omega, V_{GE} = 12 V$	10			μs

Table 7: Switching On

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r (di/dt) _{on}	Turn-on Delay Time Current Rise Time Turn-on Current Slope	$V_{CC} = 390 \text{ V, } I_{C} = 7 \text{ A}$ R _G = 10 Ω , V _{GE} = 15V, Tj= 25°C (see Figure 19)		22.5 8.5 700		ns ns A/µs
t _{d(on)} t _r (di/dt) _{on}	Turn-on Delay Time Current Rise Time Turn-on Current Slope	$V_{CC} = 390 \text{ V, } I_{C} = 7 \text{ A}$ $R_{G} = 10 \Omega$, $V_{GE} = 15 \text{ V, } Tj = 125 ^{\circ}\text{C}$ (see Figure 19)		22 9.5 680		ns ns A/µs

Table 8: Switching Off

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
$t_{\rm r}({ m V}_{ m off}) \ t_{ m d}({ m o}_{ m f}) \ t_{ m f}$	Off Voltage Rise Time Turn-off Delay Time Current Fall Time	$V_{CC} = 390 \text{ V, } I_{C} = 7 \text{ A,}$ $R_{GE} = 10 \Omega \text{ , } V_{GE} = 15 \text{ V}$ $T_{J} = 25 \text{ °C}$ (see Figure 19)		60 116 75		ns ns ns
$t_{\rm r}({ m V}_{ m off}) \ t_{ m d}({ m off}) \ t_{ m f}$	Off Voltage Rise Time Turn-off Delay Time Current Fall Time	$V_{CC} = 390 \text{ V, } I_{C} = 7 \text{ A,}$ $R_{GE} = 10 \Omega \text{ , } V_{GE} = 15 \text{ V}$ $Tj = 125 ^{\circ}C$ (see Figure 19)		24 196 144		ns ns ns

Table 9: Switching Energy

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Eon (2) E _{off} (3) E _{ts}	Turn-on Switching Losses Turn-off Switching Losses Total Switching Losses	$V_{CC} = 390 \text{ V, } I_{C} = 7 \text{ A}$ $R_{G} = 10 \Omega$, $V_{GE} = 15 \text{ V, } Tj = 25 ^{\circ}\text{C}$ (see Figure 19)		82 155 237		µJ µJ µJ
Eon (2) E _{off} (3) E _{ts}	Turn-on Switching Losses Turn-off Switching Losses Total Switching Losses	$V_{CC} = 390 \text{ V, } I_{C} = 7 \text{ A}$ $R_{G} = 10 \Omega$, $V_{GE} = 15 \text{ V, } Tj = 125 ^{\circ}\text{C}$ (see Figure 19)		131 370 501		µJ µJ µJ

⁽¹⁾ Pulsed: Pulse duration = 300 µs, duty cycle 1.5%

⁽²⁾ Eon is the turn-on losses when a typical diode is used in the test circuit in figure 2. If the IGBT is offered in a package with a co-pack diode, the co-pack diode is used as external diode. IGBTs & DIODE are at the same temperature (25°C and 125°C)

⁽³⁾Turn-off losses include also the tail of the collector current.

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Table 10: Collector-Emitter Diode

Symbol	Parameter	Test Condiction	Min.	Тур.	Max.	Unit
V _f	Forward On-Voltage	If = 3.5 A If = 3.5 A, Tj = 125 °C		1.3 1.1	1.9	V V
t _{rr} t _a Q _{rr} I _{rrm} S	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current Softness factor of the diode	If = 7 A, V_R = 40 V, T_j = 25 °C, di/dt = 100 A/ μ s		37 22 40 2.1 0.68		ns ns nC A
t _{rr} t _a Q _{rr} I _{rrm} S	Reverse Recovery Time Reverse Recovery Charge Reverse Recovery Current Softness factor of the diode	If = 7 A, V_R = 40 V, T_j = 125 °C, di/dt = 100 A/ μ s		61 34 98 3.2 0.79		ns ns nC A

Figure 3: Output Characteristics

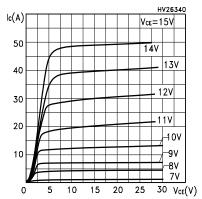


Figure 4: Transconductance

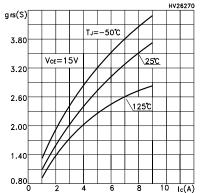


Figure 5: Collector-Emitter On Voltage vs Collector Current

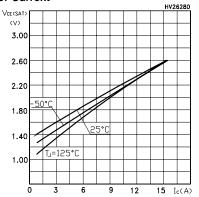


Figure 6: Transfer Characteristics

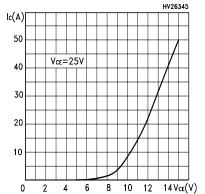


Figure 7: Collector-Emitter On Voltage vs Temperature

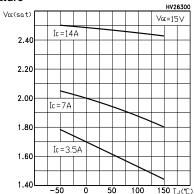


Figure 8: Normalized Gate Threshold vs Temperature

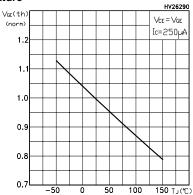


Figure 9: Normalized Breakdown Voltage vs Temperature

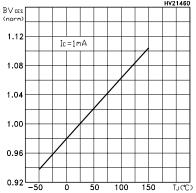


Figure 10: Capacitance Variations

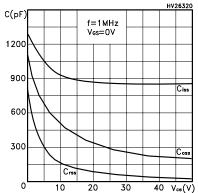


Figure 11: Total Switching Losses vs Gate Resistance

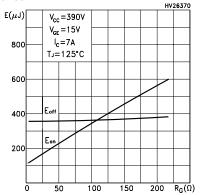


Figure 12: Gate Charge vs Gate-Emitter Voltage

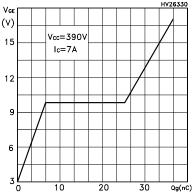


Figure 13: Total Switching Losses vs Temperature

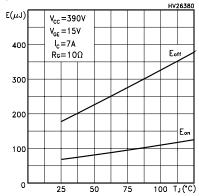
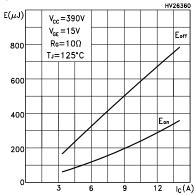


Figure 14: Total Switching Losses vs Collector Current



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Figure 15: Thermal Impedance For TO-220/ D2PAK

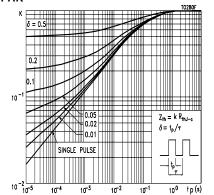


Figure 16: Thermal Impedance For TO-220FP

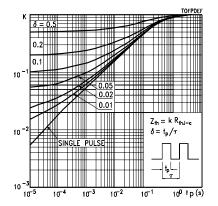


Figure 17: Turn-Off SOA

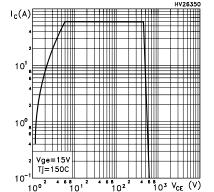


Figure 18: Emitter-Collector Diode Characteristics

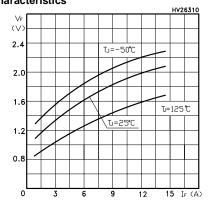


Figure 19: Test Circuit for Inductive Load Switching

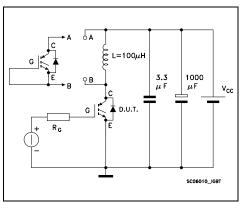


Figure 20: Switching Waveforms

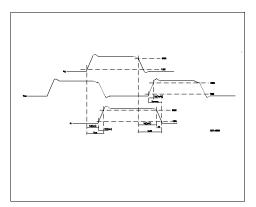


Figure 21: Gate Charge Test Circuit

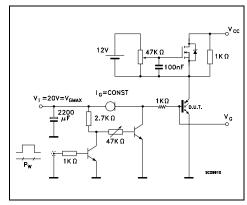
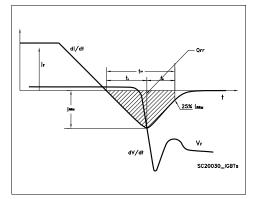
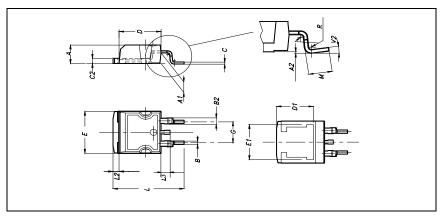


Figure 22: Diode Recovery Times Waveform



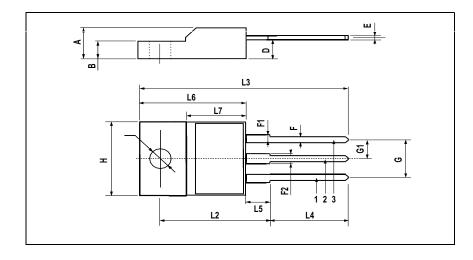
D²PAK MECHANICAL DATA

D.114		mm.		inch		
DIM.	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α	4.4		4.6	0.173		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
В	0.7		0.93	0.027		0.036
B2	1.14		1.7	0.044		0.067
С	0.45		0.6	0.017		0.023
C2	1.23		1.36	0.048		0.053
D	8.95		9.35	0.352		0.368
D1		8			0.315	
E	10		10.4	0.393		
E1		8.5			0.334	
G	4.88		5.28	0.192		0.208
L	15		15.85	0.590		0.625
L2	1.27		1.4	0.050		0.055
L3	1.4		1.75	0.055		0.068
М	2.4		3.2	0.094		0.126
R		0.4			0.015	
V2	0°		40			



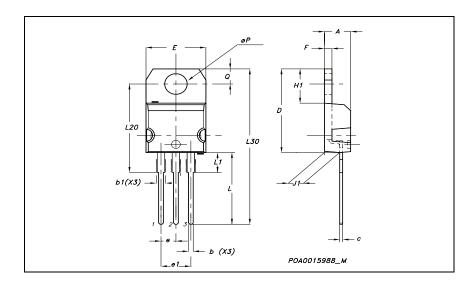
TO	-22NF	P M	FCH	ANIC	·Δ·	DATA	

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α	4.4		4.6	0.173		0.181
В	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
E	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
Н	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	.0385		0.417
L5	2.9		3.6	0.114		0.141
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126

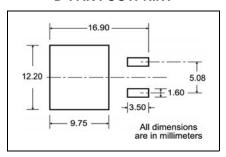


TO-220 MECHANICAL DATA

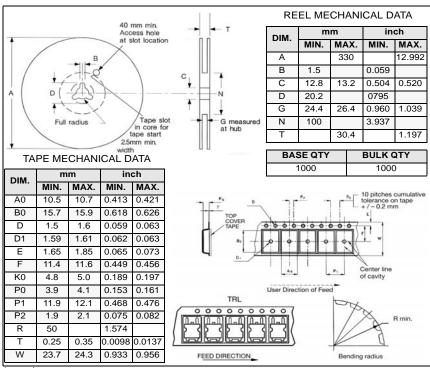
DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
Α	4.40		4.60	0.173		0.181
b	0.61		0.88	0.024		0.034
b1	1.15		1.70	0.045		0.066
С	0.49		0.70	0.019		0.027
D	15.25		15.75	0.60		0.620
E	10		10.40	0.393		0.409
е	2.40		2.70	0.094		0.106
e1	4.95		5.15	0.194		0.202
F	1.23		1.32	0.048		0.052
H1	6.20		6.60	0.244		0.256
J1	2.40		2.72	0.094		0.107
L	13		14	0.511		0.551
L1	3.50		3.93	0.137		0.154
L20		16.40			0.645	
L30		28.90			1.137	
øΡ	3.75		3.85	0.147		0.151
Q	2.65		2.95	0.104		0.116



D²PAK FOOTPRINT



TAPE AND REEL SHIPMENT



* on sales type

Table 11: Revision History

Date	Revision	Description of Changes
14-Jun-2005	1	New release
22-Jul-2005	2	Complete version

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